

Synthesizing Computable Functions from Synchronous Specifications

Sarah Winter

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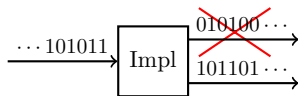
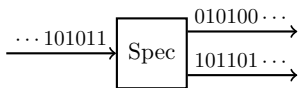
January 6, 2021
YR-OWLS, online

Reactive Synthesis of Non-terminating Systems

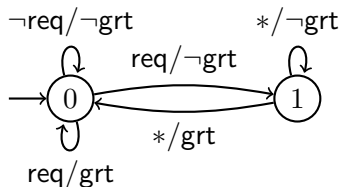
Specification $\xrightarrow{\text{synthesize}}$ **Implementation**

one input is in relation
with several outputs

algorithm that selects
a unique output for each input



Church Synthesis

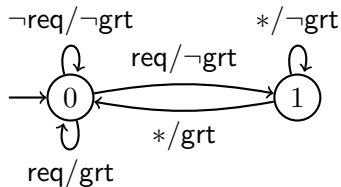


Synchronous specifications

(synchronous relations)

e.g, given by
synchronous transducers with
parity acceptance

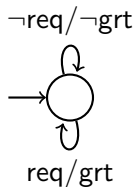
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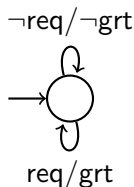
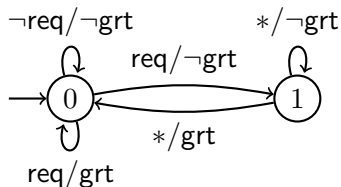
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Synchronous implementations

given by
Mealy machines

Church Synthesis



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Synchronous implementations

given by
Mealy machines

Theorem (Büchi/Landweber'69). It is decidable whether a synchronous specification is implementable by a Mealy machine.

More Relaxed Implementations

Goal Decide whether a synchronous specification is implementable (by an algorithm/a program/a deterministic Turing machine).

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- ▶ can be implemented, every deterministic machine has to wait until it sees the third input letter

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where $u \in \{a, b\}^*$, $\alpha, \beta \in \{a, b\}^\omega$, A, B are special letters

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- ▶ can be implemented, but, every deterministic machine has to wait arbitrary long to output something valid
- ▶ e.g., implemented by a deterministic machine that computes the function

$$uA\alpha \mapsto A^{|u|}\alpha \quad uB\alpha \mapsto B^{|u|}\alpha$$

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A function $f: \Sigma^\omega \rightarrow \Gamma^\omega$ is **computable** if there exists a deterministic Turing machine that

- ▶ outputs longer and longer prefixes of an acceptable output
- ▶ while it reads longer and longer prefixes of the input.

Computability

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M **computes** f if for all $\alpha \in \text{dom}(f)$:

- ▶ $\forall k: M(\alpha, k)$ is a prefix of $f(\alpha)$, and
- ▶ $\forall i \exists j: |M(\alpha, j)| \geq i$

Computability and Continuity

Computability and Continuity

A function $f: \Sigma^\omega \rightarrow \Gamma^\omega$ is **continuous** at $\alpha \in \text{dom}(f)$ if

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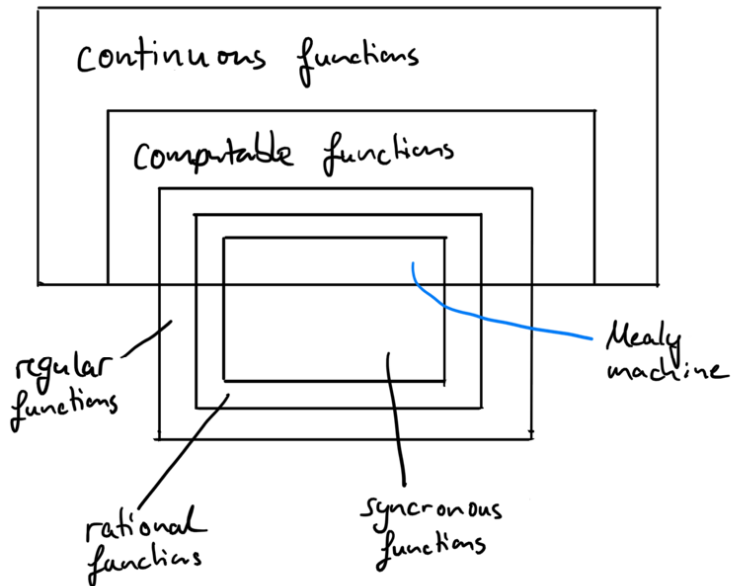
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▶ If $f: \Sigma^\omega \rightarrow \Gamma^\omega$ is computable, then it is continuous,

▶ the converse does not hold.

Computability and Continuity



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- ▶ e.g., implemented by a deterministic machine that computes the function $uA\alpha \mapsto A^{|u|}\alpha \quad uB\alpha \mapsto B^{|u|}\alpha$
- ▶ There is no way to complete the domain and remain implementable!

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Is the function computable?

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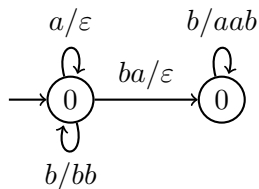
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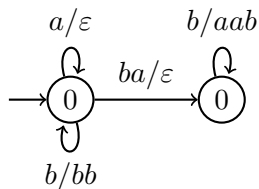
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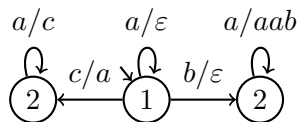
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Theorem (Filiot/W.). It is EXPTIME-complete to decide whether a continuous function can be synthesized from a given synchronous relation with **partial domain**. Such a synthesized function is computable.

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Game view

- ▶ Adam plays input letters

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Solution

- ▶ Instead of an explicit lookahead, store a finite abstraction

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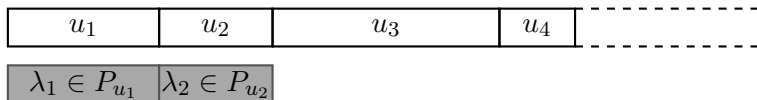
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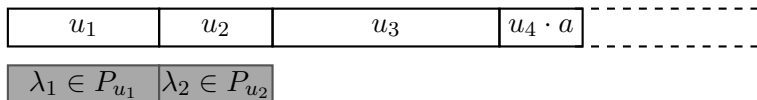


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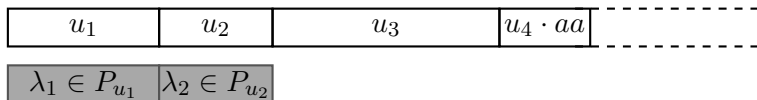


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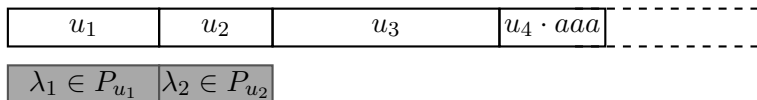


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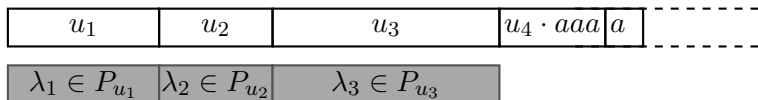
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Winning condition If Adam plays a valid input sequence,

- ▶ Eves makes a move infinitely often,
- ▶ her moves describe an accepting run wrt the specification.

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 - ▶ transducer goes right until A resp. B is read, no output
 - ▶ goes back left to the beginning, no output
 - ▶ goes right, outputs A resp. B for every letter until A resp. B is read,
 - ▶ goes right and copies the input

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 - ▶ then a deterministic machine may wait forever to output something valid.
 - ▶ Result: a finite output sequence, but the infinite input sequence is valid \nexists

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- ▶ Deterministic two-way transducers suffice, sequential transducers do not

Total vs. Partial Domain Implementations

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² Starting from a specification given by a deterministic automaton

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- ▶ Finite word setting: Undecidable whether a sequential function can be synthesized. (Carayol/Löding'14)

Undecidability Proof (similar to finite word setting)

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- ▶ Rational relation with domain $\{1, \dots, n\}^* \{a, b\}^\omega$ and pairs

$$i_1 \cdots i_m \alpha \begin{cases} \mapsto u_{i_1} \cdots u_{i_m} \beta & \text{if } \alpha \text{ contains } \infty \text{ many } a \\ \not\mapsto v_{i_1} \cdots v_{i_m} \beta & \text{otherwise} \end{cases}$$

with $i_1 \cdots i_m \in \{1, \dots, n\}^*$ and $\alpha, \beta \in \{a, b\}^\omega$.

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PCP instance has a solution

- ▶ no implementation exists
- ▶ never known whether the input sequence has ∞ many a

Work in Progress: Deterministic Rational Relations

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Class between synchronous and rational relations.

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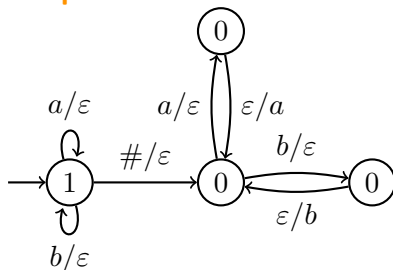
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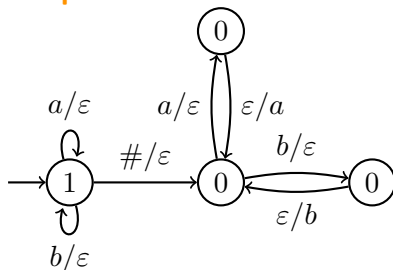
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Example.



- ▶ recognizes $f: u\#\alpha \mapsto \alpha, \quad u \in \{a, b\}^*, \alpha \in \{a, b\}^\omega$
- ▶ f is not synchronous

Work in Progress: Deterministic Rational Relations

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Almost Sure Theorem. It is decidable whether a continuous function can be synthesized from a given deterministic rational relation.

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Almost Sure Theorem. It is decidable whether a continuous function can be synthesized from a given deterministic rational relation.

Almost Sure Theorem. Such a synthesized function is computable by a deterministic two-way transducer.

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Example.

- ▶ Specification: $(a^*b\cdots, b\cdots) \quad (a^*c\cdots, c\cdots)$
- ▶ Specification is implementable, e.g., by a finite-memory machine (sequential transducer) that computes the function

$$a^*b\cdots \mapsto b^\omega \quad a^*c\cdots \mapsto c^\omega$$

Summary

Impl \ Spec	Mealy machine	sequential transducer	computable
synchronous w/ total domain	EXPTIME-c^1	EXPTIME-c^2	EXPTIME-c^2
synchronous w/ partial domain	EXPTIME-c^1	open	EXPTIME-c^2
det. rational	open	open	EXPTIME-c
rational	undecidable	undecidable	undecidable

¹ non-deterministic specification ² deterministic specification